EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5112	(372/25-33).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/09/07 12:02
L2	907	.1 and attenuat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/07 14:20
L3	98	1 and attenuat\$4 same splitter	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/07 12:03
L4	4	("6271924" "6344898" "6441356" "6525821").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/07 12:10
L5	103	1 and attenuat\$4 same (microcontroller or micro adj2 controller or controller or microprocessor or processor or cpu or computer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/07 16:15
L6	85	5 not 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/07 13:34
L7	47	1 and attenuat\$4 same pulse adj2 energy	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/07 14:20
L8	1	("5514871").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/07 15:53
L9	3	phase same antiphase same attenuator same (computer or controller or processor or microprocessor)	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/07 15:54
L10	29	phase same antiphase same attenuator	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/07 15:54
L11	3	phase same antiphase same attenuator and laser	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/07 16:02

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L12	3	antiphase same attenuator and laser	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/07 16:02
L13	12	antiphase same attenuat\$4 and laser	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/07 16:15
L14	9	13 not 12	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/07 16:02
L15	358	antiphase same (microcontroller or micro adj2 controller or controller or microprocessor or processor or cpu or computer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/07 16:15
L16	3	antiphase same (microcontroller or micro adj2 controller or controller or microprocessor or processor or cpu or computer) same laser	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/07 16:16
L17	24	antiphase same (microcontroller or micro adj2 controller or controller or microprocessor or processor or cpu or computer) and laser	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/07 16:58
L18	24	17 not 10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/07 16:16
L19	358	antiphase same (microcontroller or micro adj2 controller or controller or microprocessor or processor or cpu or computer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/07 17:01
L20	40	antiphase same (microcontroller or micro adj2 controller or controller or microprocessor or processor or cpu or computer) and attenuat\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/07 17:30
L21	2	("6166845").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/09/07 17:31
L22	2	("20040203248").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/09/07 17:34

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S43	2	"5157676".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/06 19:22
S44	2	"4092530".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/06 19:22
S45	2	"5128601".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/06 19:34
S46	2	"5128601".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/09/06 19:34
S47	1	("4972210").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/09/06 19:42
S48	42	("5128601").URPN.	USPAT	OR	ON	2006/09/06 19:59
S49	1965	(laser same phase same amplitude same frequency)	USPAT	OR	ON	2006/09/06 19:59
S50	1329	(phase same amplitude same frequency) with (controller or microcontroller or processor or cpu or computer)	USPAT	OR	ON	2006/09/06 20:01
S51	39	(phase same amplitude same frequency) with (controller or microcontroller or processor or cpu or computer) same laser	USPAT	OR	ON	2006/09/06 20:01

9/7/06 5:41:12 PM C:\Documents and Settings\PNguyen6\My Documents\EAST\workspaces\10658472.wsp